



X-1464 US
10/618,329

PATENT
Conf. No. Unknown

IN THE UNITED STATES PATENT OFFICE

Applicant: Andy T. Nguyen
Assignee: Xilinx, Inc.
Title: Counter-Based Clock Doubler Circuits and Methods
with Optional Duty Cycle Correction And Offset
Serial No.: 10/618,329 File Date: 7/11/2003
Examiner: Unknown Art Unit: Unknown
Docket No.: X-1464 US Conf. No.: Unknown

Mail Stop NON-FEE AMENDMENT
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PRELIMINARY AMENDMENT

Before taking action in the above-referenced application,
please enter the following amendments.



Modified 02-03

PTO/SB/21 (01-03)
Approved for use through 9/30/00. OMB 0851-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application / Conf. No.	10/618,329 / Unknown
	Filing Date	July 11, 2003
	First Named Inventor	Andy T. Nguyen
	Examiner Name	Unknown
	Art Unit	Unknown
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

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